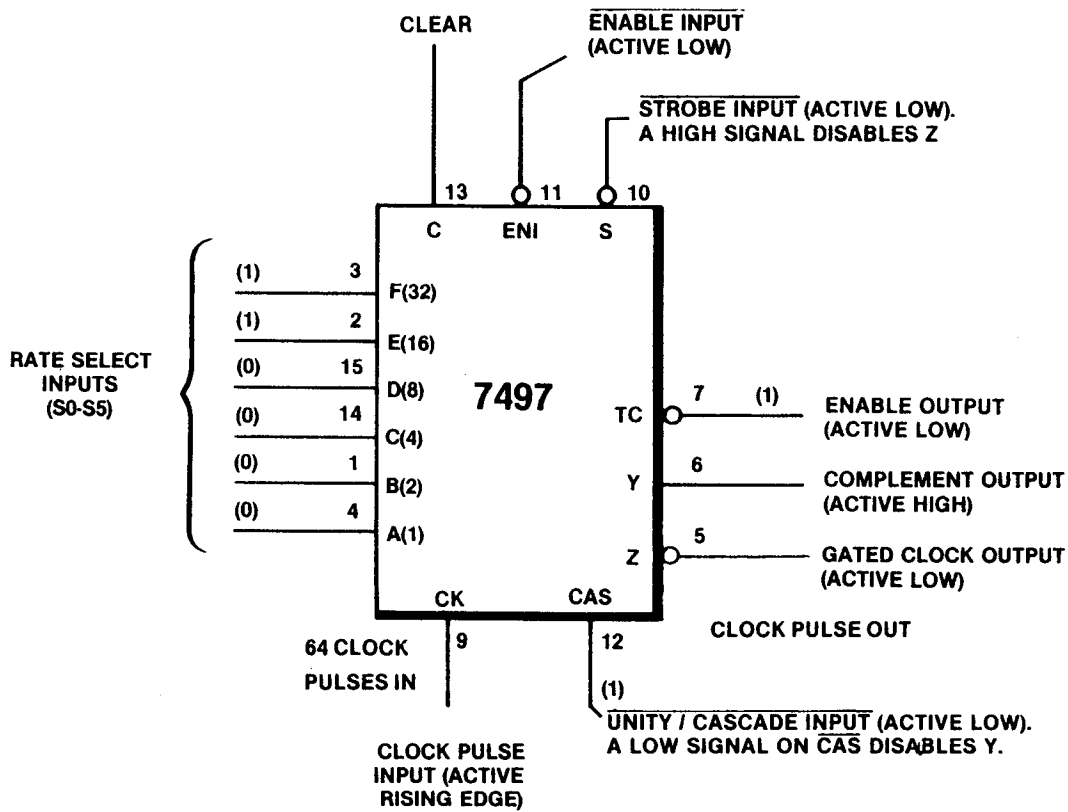


# H. Rate Multipliers

The master reset input (active high) prevents counting and resets the counter.

The output pulse rate relative to the clock frequency is determined by signals applied to the select (S0-S5) inputs.



The Z output is normally high and goes low when CK and STROBE INPUT are low and any of the inputs (S0-S5) are high. The Y output is the complement of output A and is thus normally low.

