

(11 PIN SIP)

A9		
A8		
A7	214-1	(J12-15)
A6	"-2	(" -14)
A5	"-3	(" -13)
A4	"-4	(" -12)
A3	"-5	(I12-15)
A2	"-6	(" -14)
A1	"-7	(" -13)
A0	"-21	(" -12)

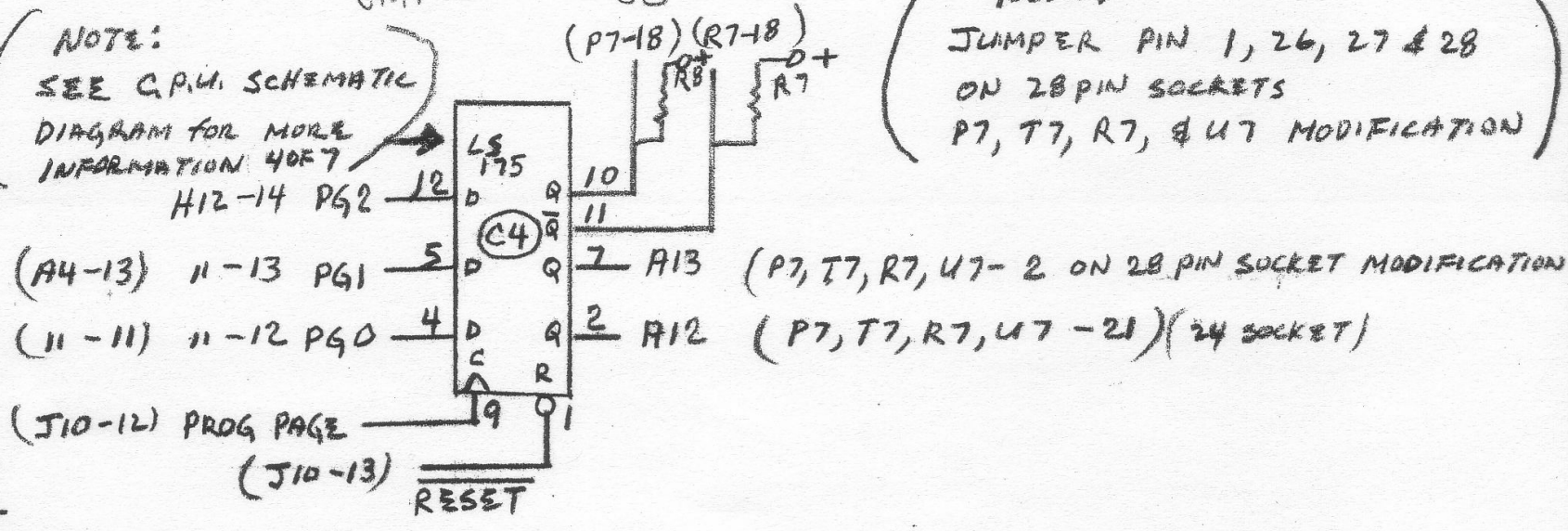
MT1

		<u>E</u>	<u>O</u>
SETP ϕ		8 ϕ	
OUT ϕ			9 ϕ
SETP F		8F	
LDAI 5			ϕ 5
ABI 55		2 ϕ	55
STA A		DA	
LDA A			AA
LDAI ϕ		4 ϕ	$\phi\phi$
JMP		5B	

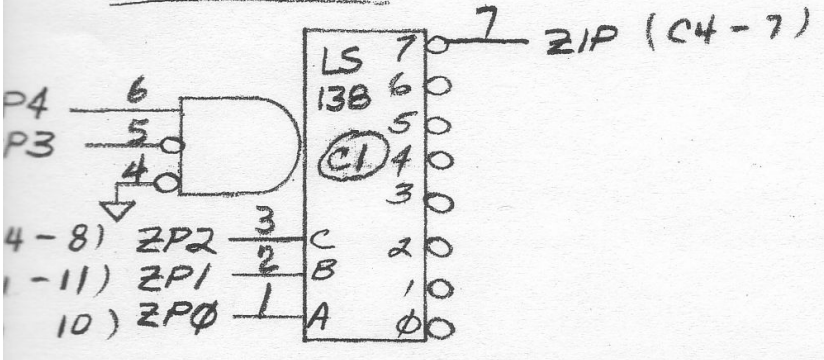
CONNECTOR)

NOTE:
SEE C.P.U. SCHEMATIC
DIAGRAM FOR MORE
INFORMATION 40F7

NOTE:
JUMPER PIN 1, 26, 27 & 28
ON 28 PIN SOCKETS
P7, T7, R7, & U7 MODIFICATION



FRAME OUT
PRE-DETECT



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