TROUBLESHOOTING

Foreground on the Gottlieb GG-III Video System is generated on a 16x16 pixel format. Foreground characters can be moved to any point on the CRT with each having its own priority scheme in relationship to the other foreground characters. The foreground characters consist of: The word "Q*bert" on the instruction frame, all moving characters, the large letters for high-

est score on the high score table, Q*berts balloon that appears when he collides with an enemy and the level number that is displayed between levels.

Background is generated on an 8x8 pixel format and is behind all foreground with the exception of a priority switch (when Q*bert falls off behind the pyramid).

It is no secret that solid state hardware does sometimes become defective. It is for this reason that the accompanying table of symptom/possible causes can be helpful when troubleshooting the Gottlieb GG-III Logic Board. A note to remember: This list has been compiled to assist the technician in troubleshooting the Logic Board. The list of possible faults is not always definitive.

NO FOREGROUND CHARACTERS ON SCREEN FOREGROUND CHARACTERS DIVIDED HORIZ. AND G17, H10, L12 STACKED ON TOP OF EACH OTHER TWO SEPARATE CHARACTERS APPEAR STACKED VERT.

THE WORD "Q*BERT" IS SEPARATED INTO SECTIONS THE WORD "Q*BERT" APPEARS AS HEX NUMBERS INCORRECT CHARACTERS APPEAR (I.E., SLICK IN PLACE OF Q*BERT)

SYMPTOMS

FOREGROUND

FOREGROUND CHARACTERS ARE BLURRY (DISTORTED) HORIZ. LINES THROUGH FOREGROUND CHARACTERS UNEVEN MOVEMENT OF CHARACTERS

RIGHT HALF OF CHARACTER APPEARS ON THE LEFT SIDE OF THE SAME CHARACTER VERT. LINE ABOVE Q*BERT THAT BLANKS EVERYTHING ABOVE Q*BERT
FOREGROUND DIVIDED VERT.(MIRROR IMAGE OF SELF)

FOREGROUND FROZEN (NO MOVEMENT) INCORRECT FOREGROUND COLORS CHARACTERS APPEAR AS COLORED SQUARES

BACKGROUND

NO BACKGROUND CHARACTERS ON SCREEN

INCORRECT LETTERS GENERATED ON THE SCREEN PURPLE SQUARE APPEARS AT BOTTOM OF LETTERS

DISTORTED LETTERS ON TOP HALF OF THE SCREEN RANDOM LETTERS FLASHING RANDOMLY ON SCREEN BACKGROUND CHARACTERS ARE BLURRY (DISTORTED)

JUMBLED BACKGROUND PYRAMID DIVIDED INTO SEVERAL VERT. SECTIONS HORIZ. LINES DIVIDE CHARACTERS

GREEN BACKGROUND WHEN Q*BERT IS SMASHED INCORRECT BACKGROUND COLORS

POSSIBLE CAUSES

D2,E1-2,E2-3,E4,G8,J10,J12,K11

E1-2, E2-3, E4

SIP 71 J1, J2, K5 E1-2, E2-3, E4, G4, J1, K1-K3

K10, L7-8 SIP 72, SIP 73, H3, H4

Н3

G17

L10

G13,G14,G15,H12,K5,K10,Q82-Q87 K1-K6, K7-8, L4-5, L5-6, L6-7, L7-8

POSSIBLE CAUSES

D5,D10,E8,E10-11,E11-12,

E13,G6,J7,J8,J12 E10-11

D11, E7, E8, E9-10, J8

E10-11, E11-12, E13 E10-11, E11-12, E13

E10-11, E16 G11

G13, G14, G15, Q82-Q87

The Intel 8088 microprocessor is a third generation microprocessor with an 8-bit data bus to memory and to I/O (Input/Output). chip is a standard 40 pin dual inline package (see FIG. 1) and operates from a single +5VDC power source. The 8088 is extremely flexible in its application and is well suited for use in the GG-III System.

The processor has dual operating modes (minimum and maximum) which is allowed by dual function pins selected by a strapping pin. The GG-III System utilizes the minimum mode of operation. In this mode, these dual function pins transfer control signals directly to memory and I/O devices.

The high efficiency of the 8088 is conducive to combining a 16-bit internal bus with a pipeline architecture allowing instructions to be prefetched during spare bus cycles. Microprocessors execute a program by repeating the simplified cycle shown below:

- 1. Fetch the next instruction from memory.
- 2. Read the operand (if required by the instruction).
- 3. Execute the instruction.
- 4. Write the results (if required by the instruction).

These steps are usually performed serially by most microprocessors. The architecture of the 8088 however, allocates these steps to two separate processing units within the CPU. The execution unit executes instructions while the bus interface unit fetches instructions, reads operands and writes results. Both units work independently of each other and are able to overlap instruction fetch with execu-This means that tion. the time required to fetch an instruction, during normal program sequence, disappears because the execution unit executes instructions that have already been fetched by the bus interface unit.

Below are listed several of the functions allowed by the minimum mode of the Intel 8088 microprocessor as applied to the GG-III System: The NMI (Non-Maskable Interrupt), pin 17, will receive pulses 61 times a second. The pulse is generated when the CRT's vertical blanking time begins. During this blanking time the background register (E7) transfers data to the background buffer (E10-11) through DMA (Direct

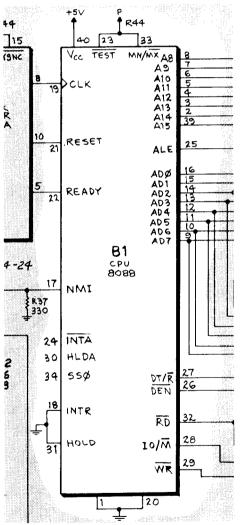


FIGURE 1.

Memory Access) for the The DT/\overline{R} next frame. (Data Transmit/Receive), pin 27, controls the direction of data flow via the Data Transceivers (C4) DIR (Direction Control Input) pin. This allows data to flow from the A bus to the B bus or from the B bus to the A bus. The DEN (Data Enable), pin 26, allows or disables data flow by placing a voltage level on the \overline{G} (Enable Input), pin 19, on the Data Transceiver (C4) so that the bus is effectively isolated. The RD (Read Control), pin 32, manages the OE (Output Enable) of the program ROM's as well as enabling the output of the Background Character Register (E7) and the Input Port Select (B10). The IO/M (IO/Memory Control), pin 28, is utilized to differentiate between either program memory or I/O on the processors bus. The WR (Write Control), pin 29, controls the read/write function of the system RAM as well as clocking the Output Port Flip Flops (A8, A9, A10).

This is a general pin function description that can be utilized when troublshooting the GG-III System.
The other pins on the microprocessor are all self explanatory on the illustration (see FIG.1).

The following page is a tear-off, postage paid, self-addressed question-naire. We would sincerely appreciate your time in answering it

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2. W	HICH AREA OF THE AN OPERATOR	nusement indust distributor		J IN? (CIRCLE ALL IANUFACTURER	THAT APPLY) ARCAL	DE OWNER	SER√	ICE PERSON
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	PPROXIMATELY HOW N RIOR TO SPIDERMAN)?			BALL GAMES ARE S	system one			
7. H	OW OFTEN DO YOU L NEVER (NOT AT ALL)	OCCASIONALL		LINE? (CIRCLE ON SOME OF THE TIME		DUENTLY		ALWAYS RY OFTEN)
3. H	OW OFTEN HAS THE C NEVER (NOT AT ALL)	OCCASIONALL		PED YOU FIND YO Some of The Time		? (CIRCLE ONE)		ALWAYS RY OFTEN)
9. A	T WHAT POINT IN TROUBLESHOOTING A GAME DO YOU CALL THI BEFORE LOOKING AFTER FINDING FOR THE PROBLEM THE FAULT DIS				"800" HOT LINE? (CIRCLE ONE) FTER TALKING TO AFTER TRYING TO RIBUTOR PERSONNEL FIX THE PROBLEM			
). W	/HAT EQUIPMENT DO JUMPER WIRES	YOU USE FOR ON- METER LOGIC P				LL THAT APPLY JRE ANALYZER		UKE 9010A
1. W	/HAT EQUIPMENT DO JUMPER WIRES	you use for for meter logic p		SHOP TROUBLESH OSCILLOSCOPE		rcle all that Jre analyze r		UKE 9010A
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4. H	NOW OFTEN IS THE INF	ORMATION WRITT		"ON TARGET" NE		d Basic? (Circi Duently		ALWAYS
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6. F	HOW OFTEN WOULD Y NEVER	ou use signatur occasionall		JPPLIED THEM ON SOMETIMES		ATICS? (CIRCLE QUENTLY		ALWAYS
7. V	VHAT WOULD YOU LIK	e to see added 1	O OUR MA	nuals?				

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9609 BERKSHIRE OT SE
OLYMPIA, WA 98505