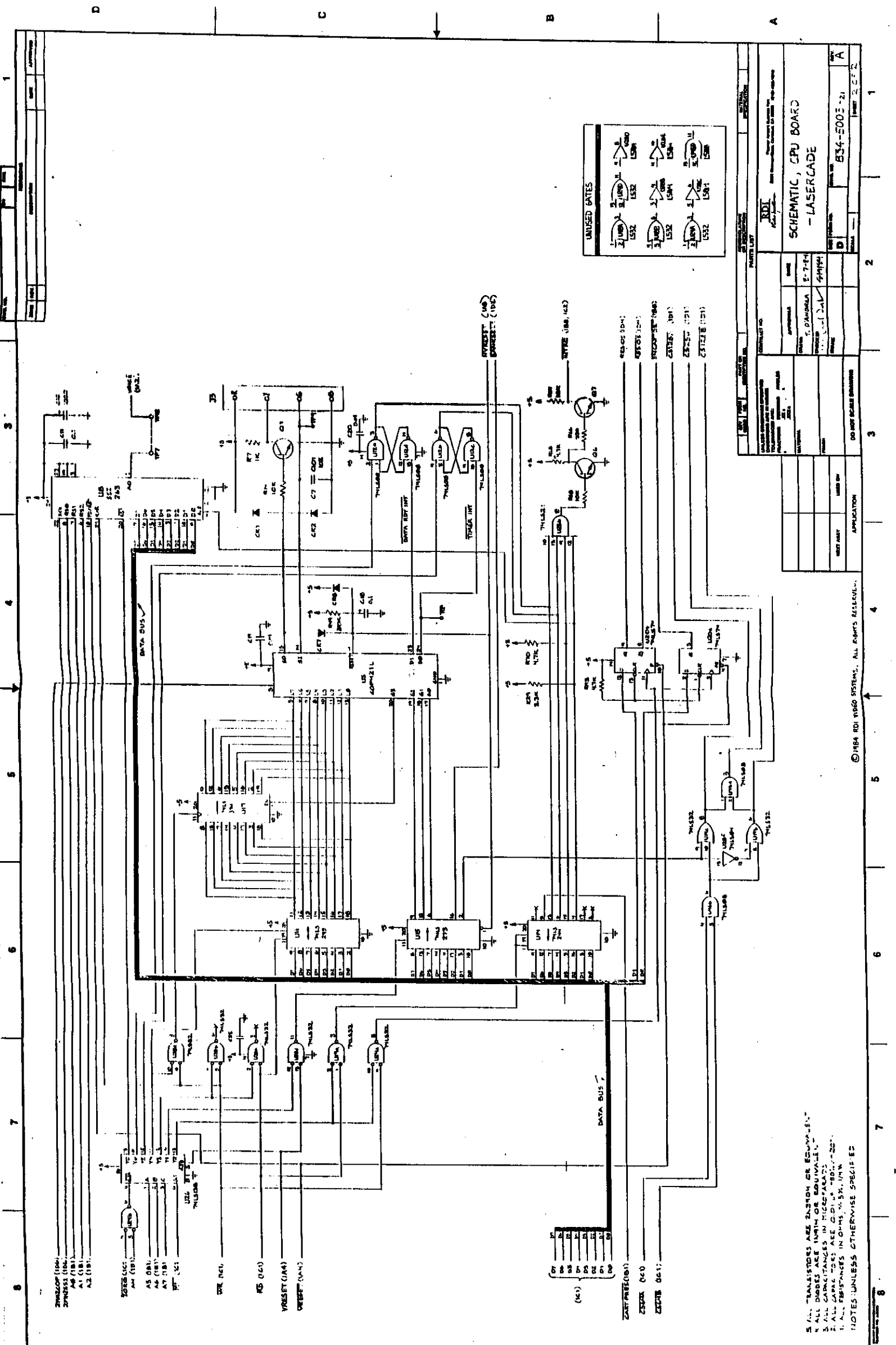


SCHEMATIC, CPU BOARD - LASERCADE	
DATE: _____ DRAWN BY: _____ CHECKED BY: _____ APPROVED BY: _____	PART NO. 834-5003-21 REV. D SHEET 1 OF 2

1 ALL TRANSISTORS ARE 2N3904 UNLESS OTHERWISE SPECIFIED.
 2 ALL CAPACITORS ARE 50V UNLESS OTHERWISE SPECIFIED.
 3 ALL RESISTORS ARE 1/4W 5% UNLESS OTHERWISE SPECIFIED.

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UNUSED GATES

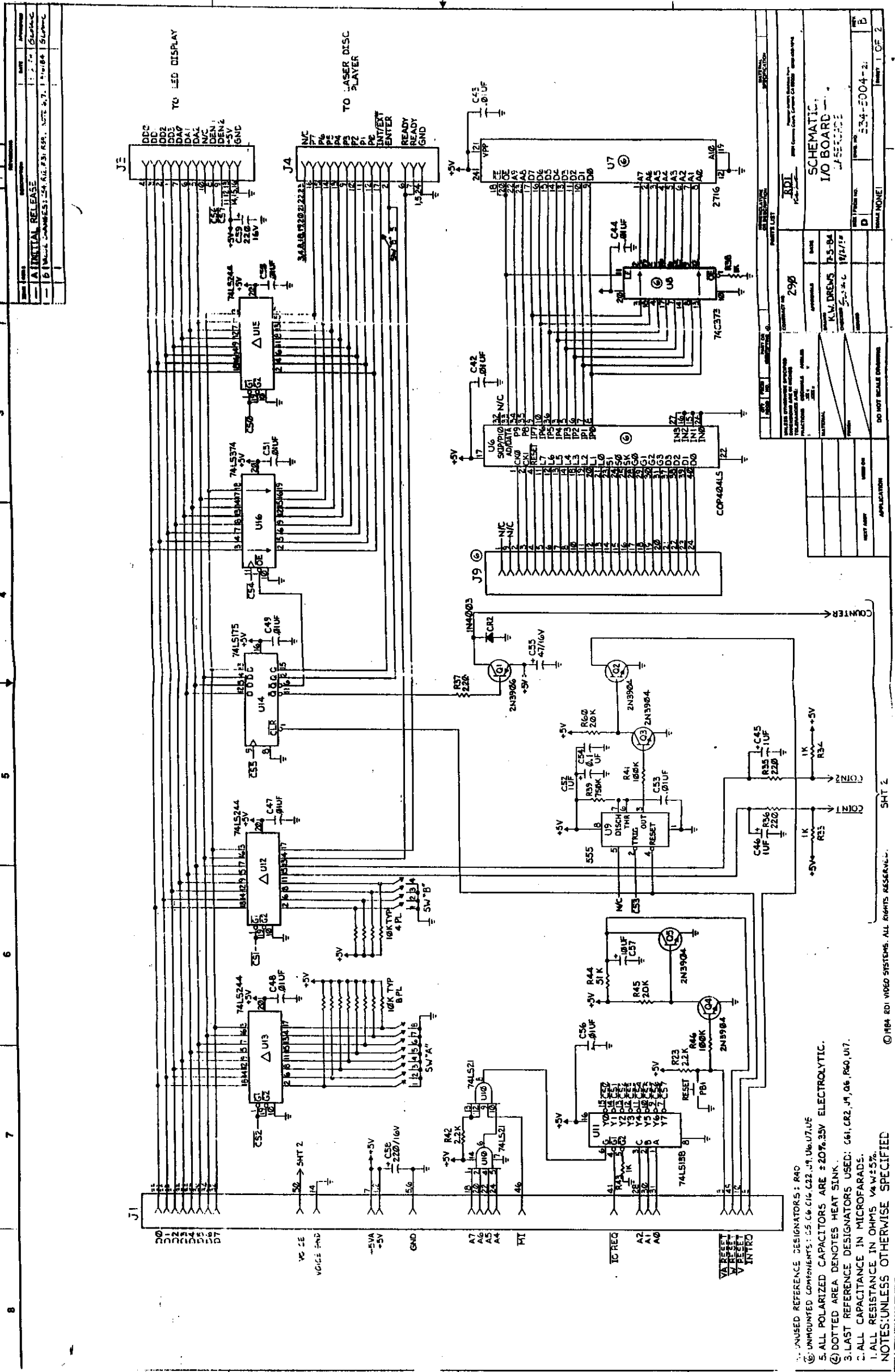
1 74100	2 74100	3 74100	4 74100	5 74100	6 74100
7 74100	8 74100	9 74100	10 74100	11 74100	12 74100

RDI		SCHEMATIC, CPU BOARD - LASERCADE	
DATE	REV	DESIGNED BY	CHKD BY
		T. D. HARRIS	S. J. HARRIS
APP'D BY	DATE	REV	REV
PART NUMBER		REV	
834-5003-21		A	

DESIGNED BY	DATE	REV	REV
T. D. HARRIS	5-7-84		
APP'D BY	DATE	REV	REV
S. J. HARRIS	5/11/84		
TESTED BY	DATE	REV	REV
APPLICATION	REV	REV	REV
DO NOT SCALE DIMENSIONS			

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5 ALL TRANSISTORS ARE SILICON OR EQUIVALENT
 6 ALL DIODES ARE SILICON UNLESS OTHERWISE SPECIFIED
 7 ALL CAPACITORS ARE IN PICOFARADS
 8 ALL RESISTORS ARE 0.1% 1/4" 800V 20W
 9 ALL DIMENSIONS IN DIMS OR 3/16" UNLESS OTHERWISE SPECIFIED



REVISION	DATE	APPROVED
A INITIAL RELEASE	12-7-84	SL/PL/AC
B	1-17-84	SL/PL/AC

COMPONENT NO.	290
APPROVALS	
DESIGNER	K.M. DREMS
CHECKER	S.S. C. L.
DATE	12/17/84
TEST APP.	
APPLICATION	
DO NOT SCALE DIMENSIONS	
SCALE	
PROJECT NO.	554-5004-2
REV.	B
OF	2

SCHEMATIC I/O BOARD LASERSCOPE

UNMOUNTED COMPONENTS: 55, C6, C16, C22, J9, U6, U7, U8
 ALL POLARIZED CAPACITORS ARE ±20% .35V ELECTROLYTIC.
 DOTTED AREA DENOTES HEAT SINK.
 LAST REFERENCE DESIGNATORS USED: C61, CR2, J9, Q6, R40, U17.
 ALL CAPACITANCE IN MICROFARADS.
 ALL RESISTANCE IN OHMS UNLESS OTHERWISE SPECIFIED.

NOTES: UNLESS OTHERWISE SPECIFIED

APPENDIX C

ARCADE MAIN HARNESS DEFINITIONS

FROM	PIN	TO	PIN	SIGNAL
LF	HOT	SW	COMMON	AC LINE HOT
SW	N/OPEN	PS1	3	AC LINE HOT
SW	N/OPEN	VDP	HOT	AC LINE HOT
LF	NEUTRAL	PS1	2	AC LINE NEUTRAL
LF	NEUTRAL	VDP	NEUTRAL	AC LINE NEUTRAL
PS2	1	ISO	PRI	115V AC #1 HOT
PS2	3	LAMP	HOT	115V AC #2 HOT
PS2	3	FAN	HOT	115V AC #2 HOT
PS2	2	ISO	PRI	115V AC #1 NEUTRAL
PS2	4	LAMP	NEUTRAL	115V AC #2 NEUTRAL
PS2	4	FAN	NEUTRAL	115V AC #2 NEUTRAL
MON	FRAME	LAMP	FRAME	FRAME GROUND
PS1	1	MON	FRAME	FRAME GROUND
LF	FRAME	PS1	1	FRAME GROUND
LF	FRAME	CPU	FRAME	FRAME GROUND
CPU	FRAME	COIN	3	FRAME GROUND
COIN	3	OCP	9	FRAME GROUND
OCP	9	CP	7	FRAME GROUND
PS1	9	CPU	3	+5V
PS1	10	CPU	4	+5V
PS1	11	CPU	7	+5V RETURN
PS1	7	CPU	30	+25V
PS1	12	CPU	8	+25V RETURN
PS1	5	COIN	8	6.3V AC LAMPS
PS1	4	COIN	9	6.3V AC LAMPS RETURN
COIN	1	CPU	10	COIN SLOT 0
COIN	2	CPU	14	COIN SLOT 1
COIN	7	CPU	11	COIN RETURN
CP	2	CPU	6	AUX GROUND
CP	3	CPU	2	AUX +5V
CP	4	CPU	1	NOT USED
CP	5	CPU	29	NOT USED
CP	6	CPU	5	HEADPHONE (LEFT)
CP	8	CPU	9	NOT USED
CP	9	CPU	13	NOT USED
CP	1	CPU	12	HEADPHONE RETURN (LEFT)
OCP	4	CPU	33	COIN COUNTER
OCP	8	CPU	32	COIN COUNTER RETURN
OCP	3	CPU	J8CENTER	L VOLUME OUT
OCP	1	CPU	J8SHIELD	L VOLUME OUT RETURN
OCP	5	CPU	J7CENTER	R VOLUME OUT
OCP	7	CPU	J7SHIELD	R VOLUME OUT RETURN

FROM	PIN	TO	PIN	SIGNAL
OCP	2	CPU	24	L VOLUME IN
OCP	1	CPU	23	L VOLUME IN RETURN
OCP	6	CPU	16	R VOLUME IN
OCP	7	CPU	15	R VOLUME IN RETURN
CPU	28	SPKR	L+	SPEAKER (LEFT)
CPU	27	SPKR	L-	SPEAKER RETURN (LEFT)
CPU	20	SPKR	R+	SPEAKER (RIGHT)
CPU	19	SPKR	R-	SPEAKER RETURN (RIGHT)

PS1 = POWER SUPPLY 12 PIN CONNECTOR
 PS2 = POWER SUPPLY 4 PIN CONNECTOR
 FAN = COOLING FAN
 SW = POWER SWITCH
 CPU = CIRCUIT BOARD MODULE
 LAMP = FLUORESCENT LAMP (MARQUEE)
 CP = CONTROL PANEL
 SPKR = SPEAKERS
 COIN = COIN DOOR
 MON = MONITOR
 VDP = VIDEODISC PLAYER
 LF = AC LINE FILTER
 OCP = OPERATOR CONVENIENCE PANEL
 ISO = MONITOR ISOLATION TRANSFORMER

NOTE: GAMES EQUIPPED WITH PR7820 DISC PLAYERS HAVE DISC PLAYER FRAME GROUND TIED TO LINE FILTER FRAME GROUND.