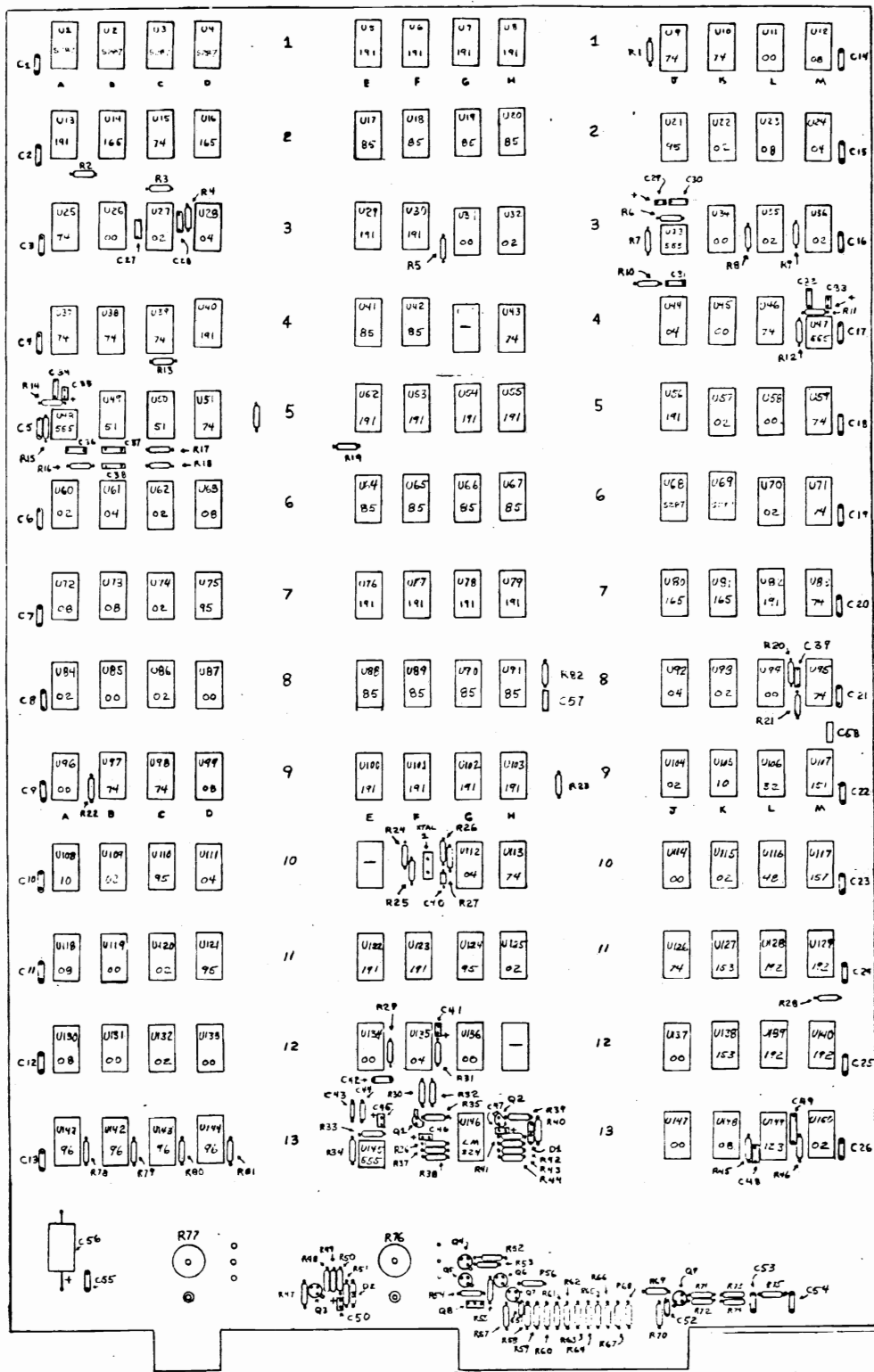


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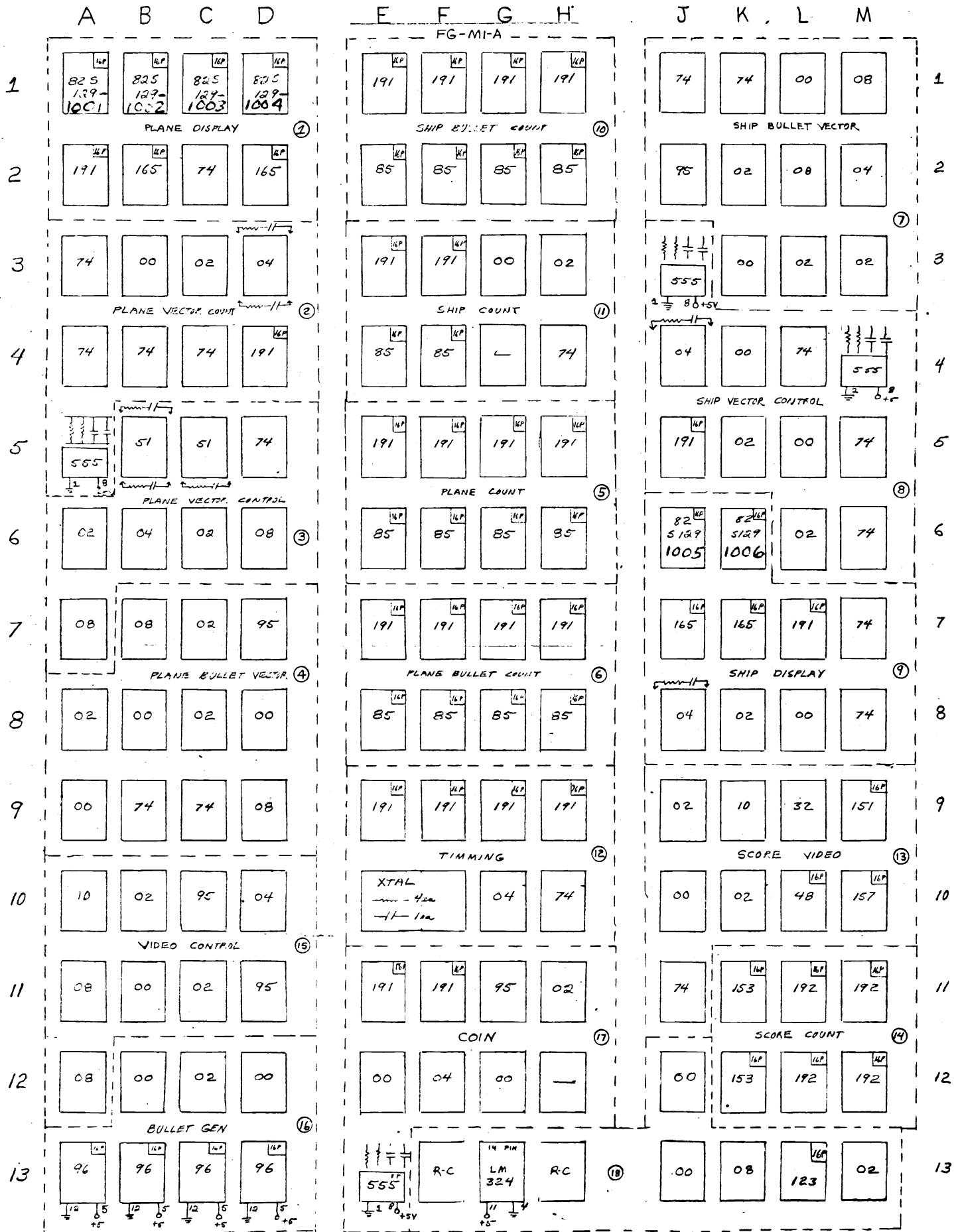
BY *Evidy*

LOGIC DIAGRAM 1A



COMPONENT LOCATION

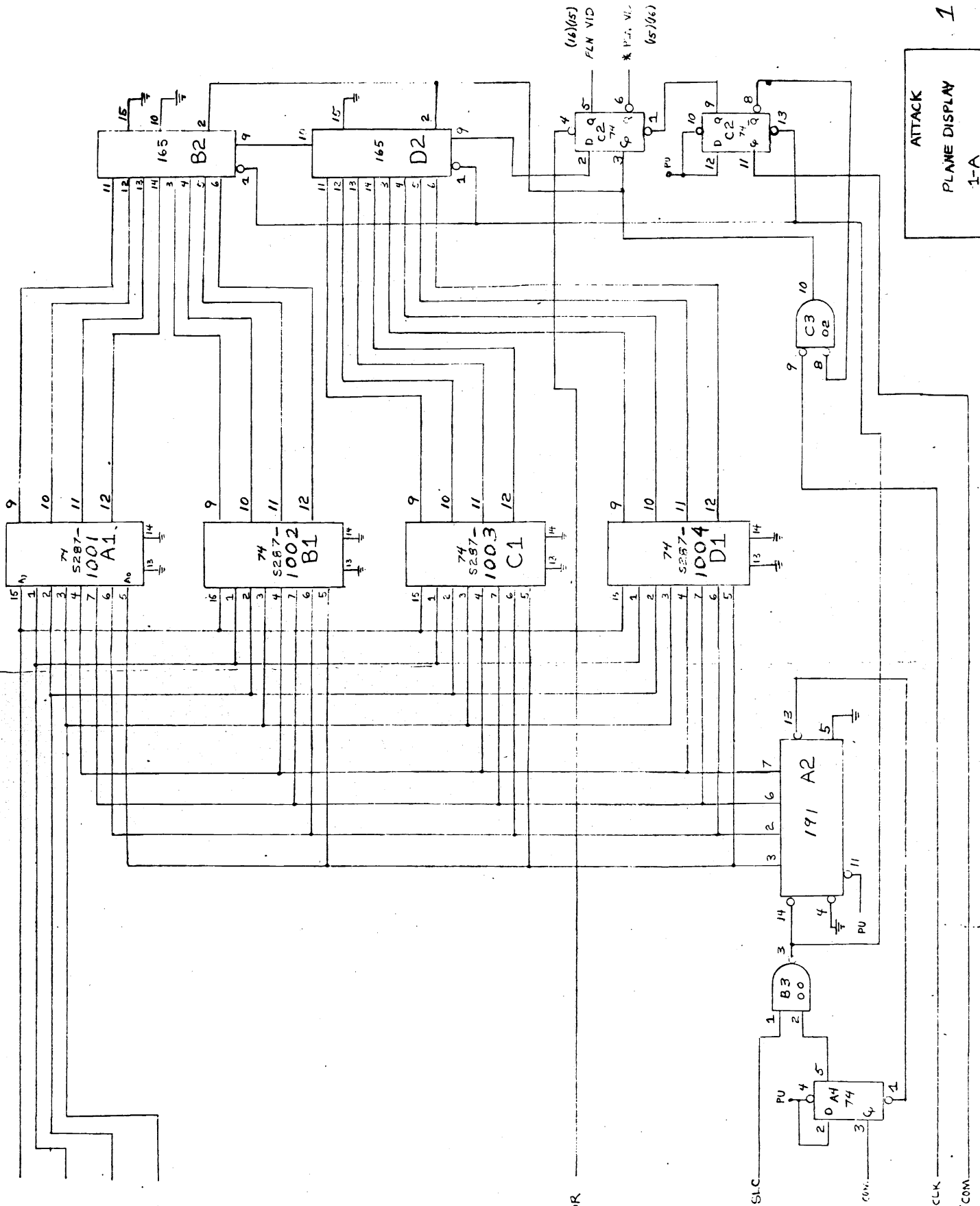
1-A ATTACK



1-A
ATTACK

IC LOCATION

- (2) PV-D
- (2) PV-C
- (2) PV-B
- (2) PV-A



(17) *POR

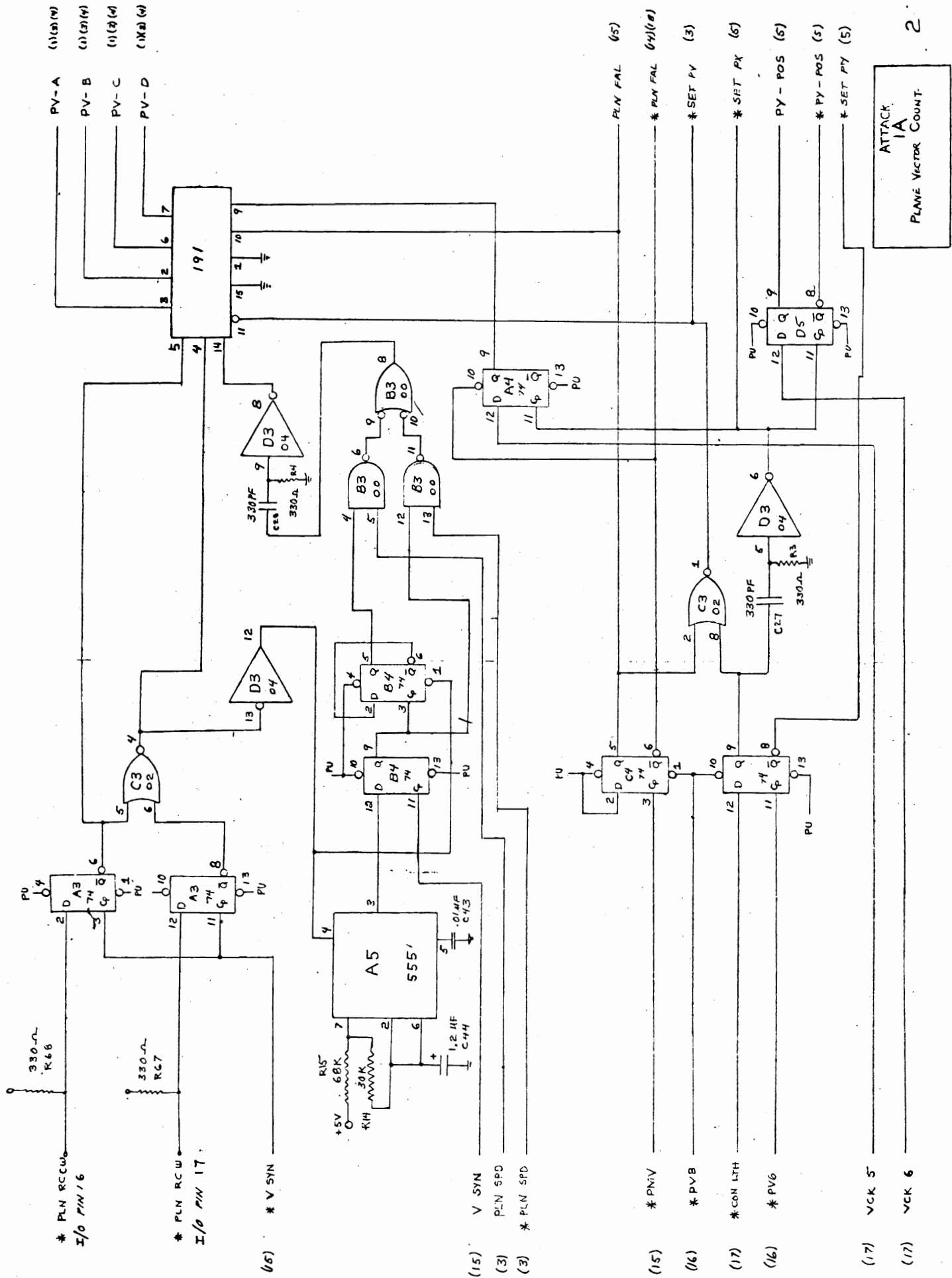
(12) SLC

(5) PY COM

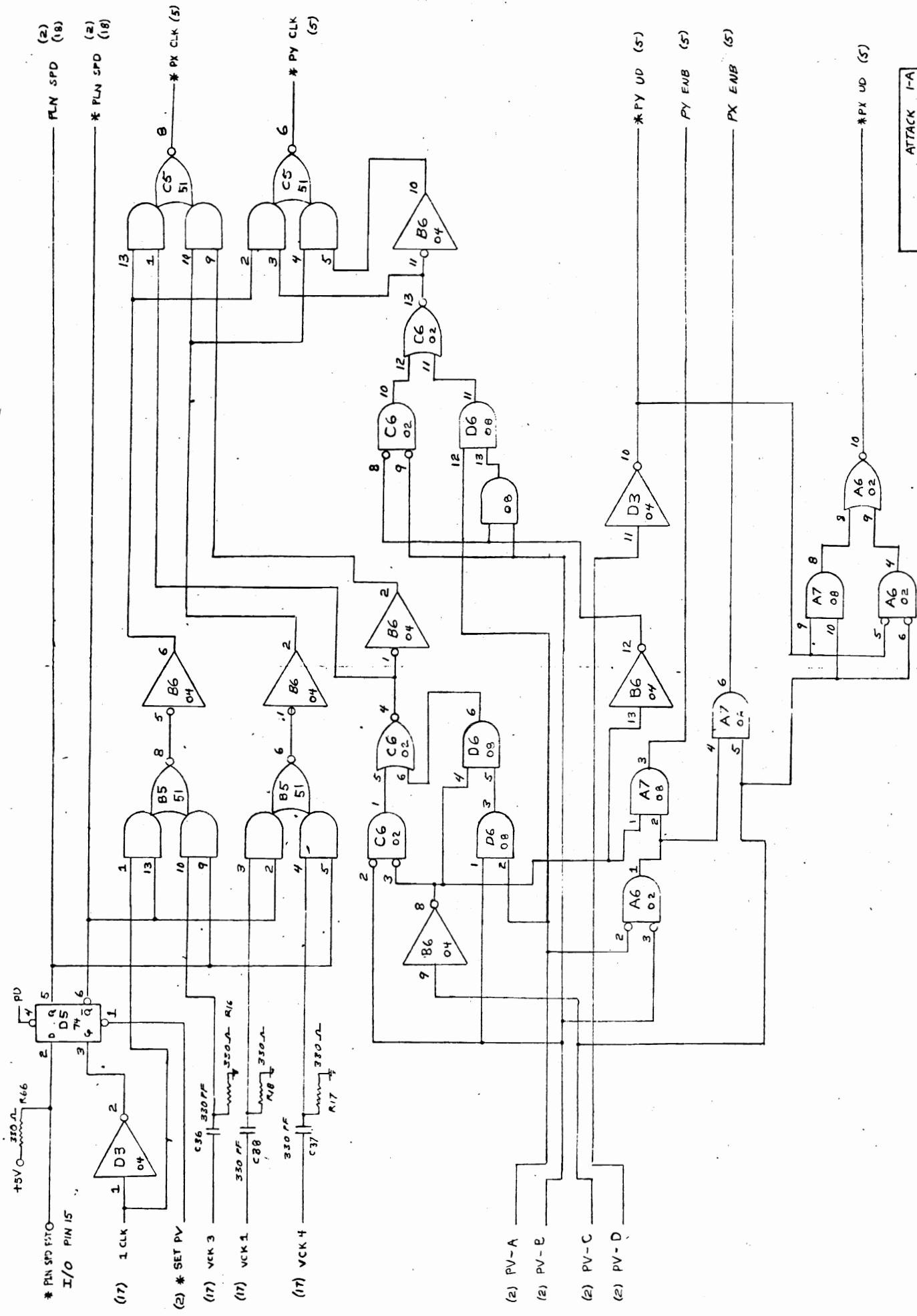
(12) *BIT CLK

(5) PX COM

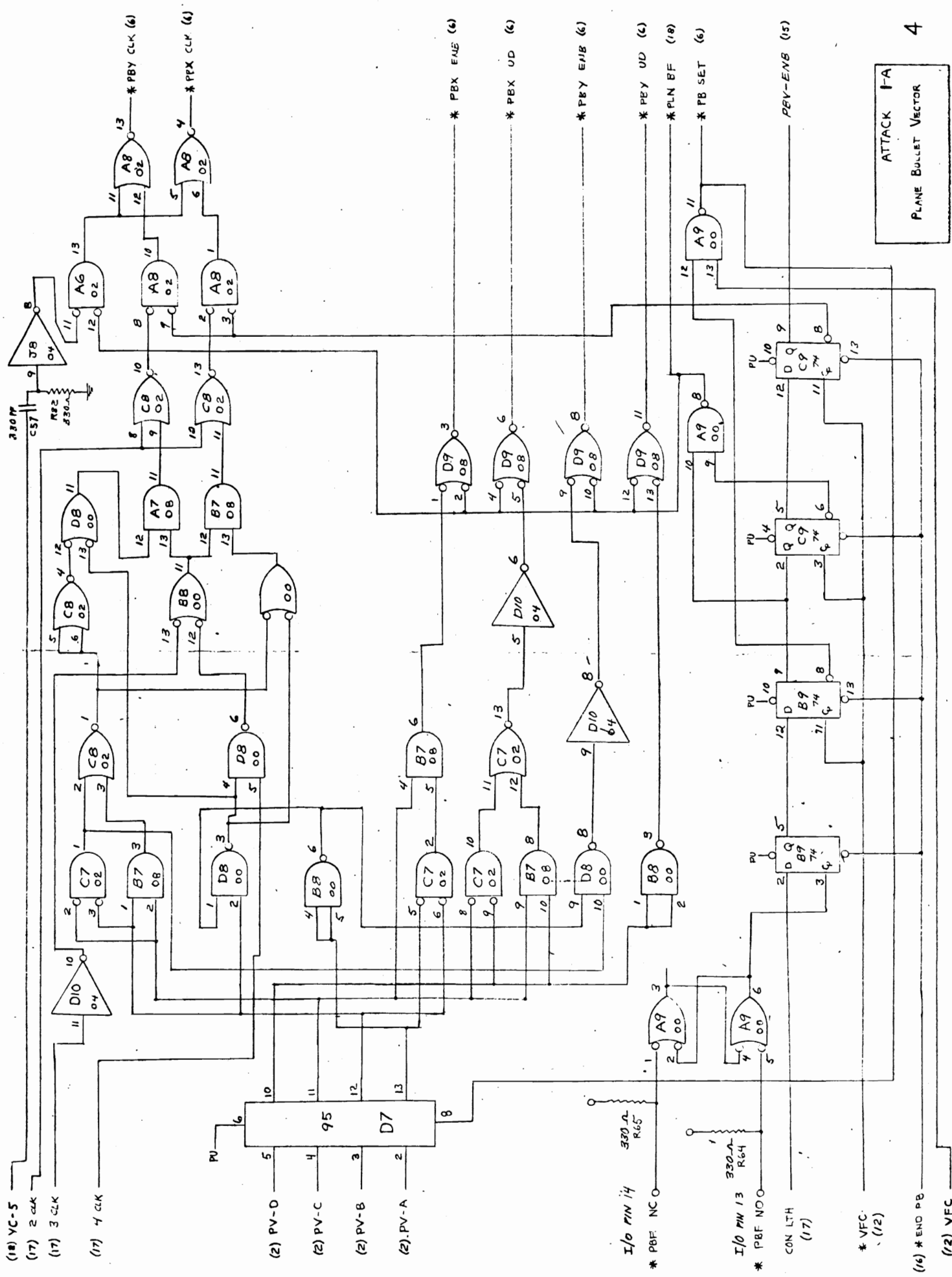
ATTACK
PLANE DISPLAY
1-A



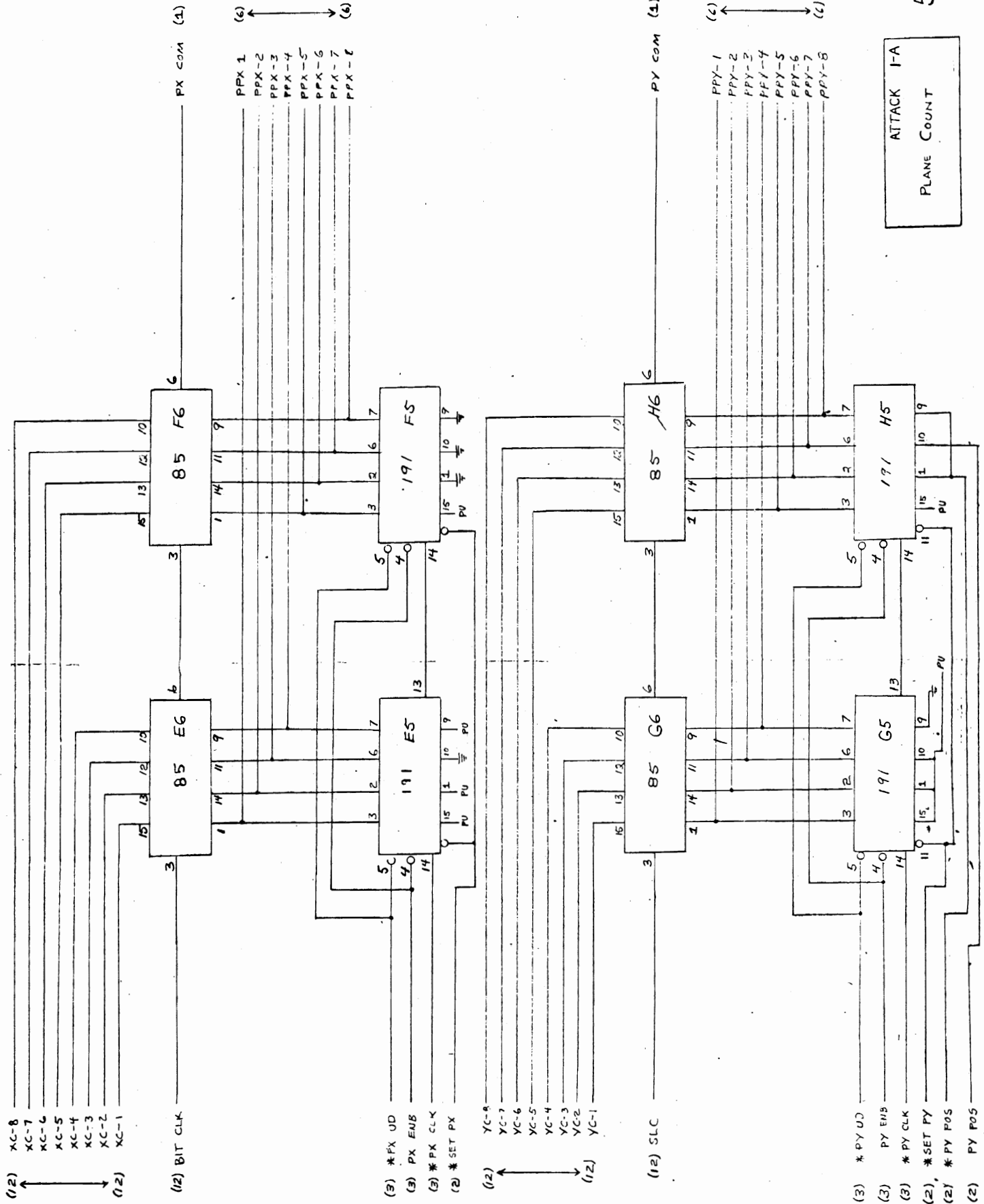
ATTACK
IA
PLANE VECTOR COUNT

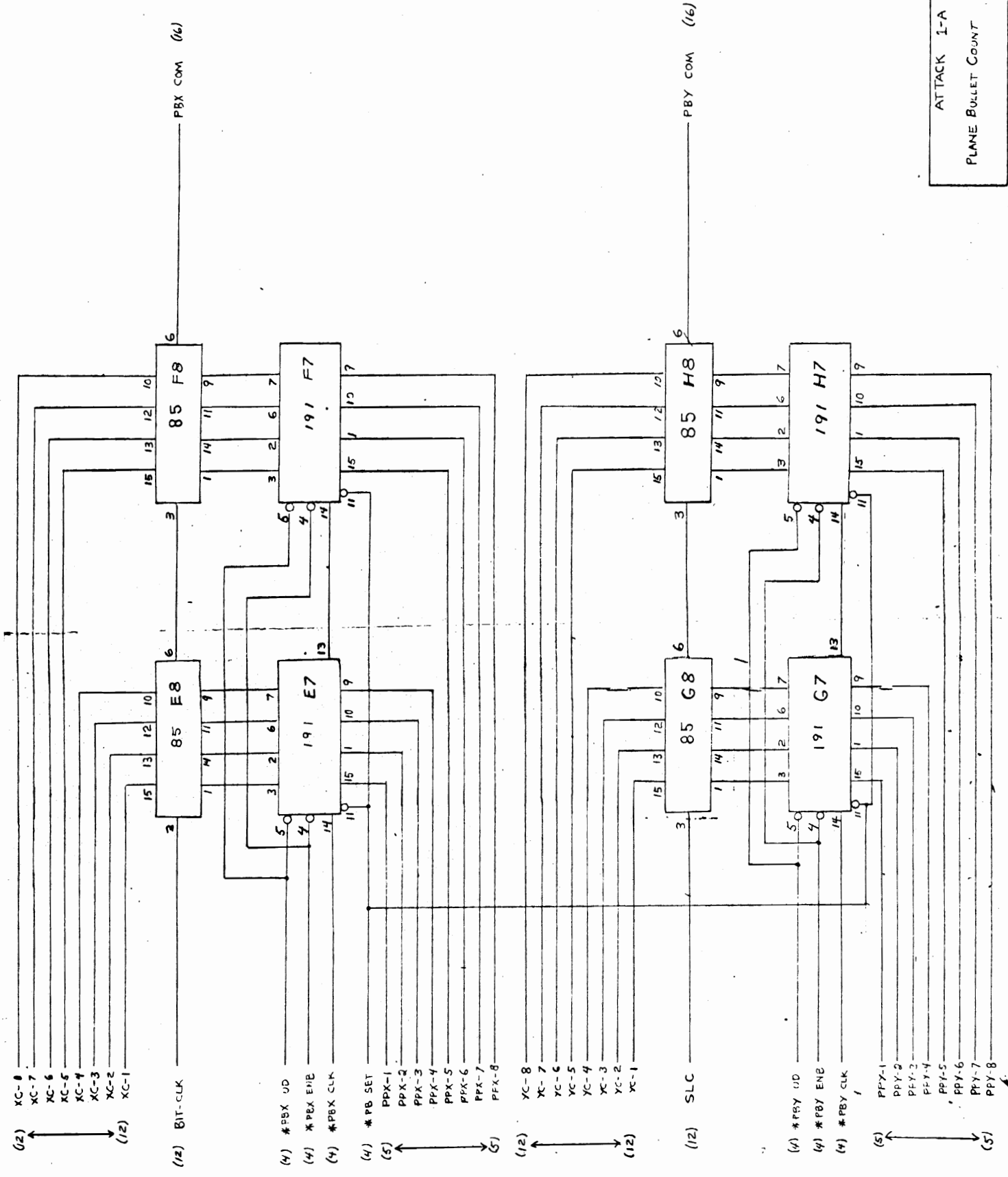


ATTACK 1-A
PLANE VECTOR CONTROL

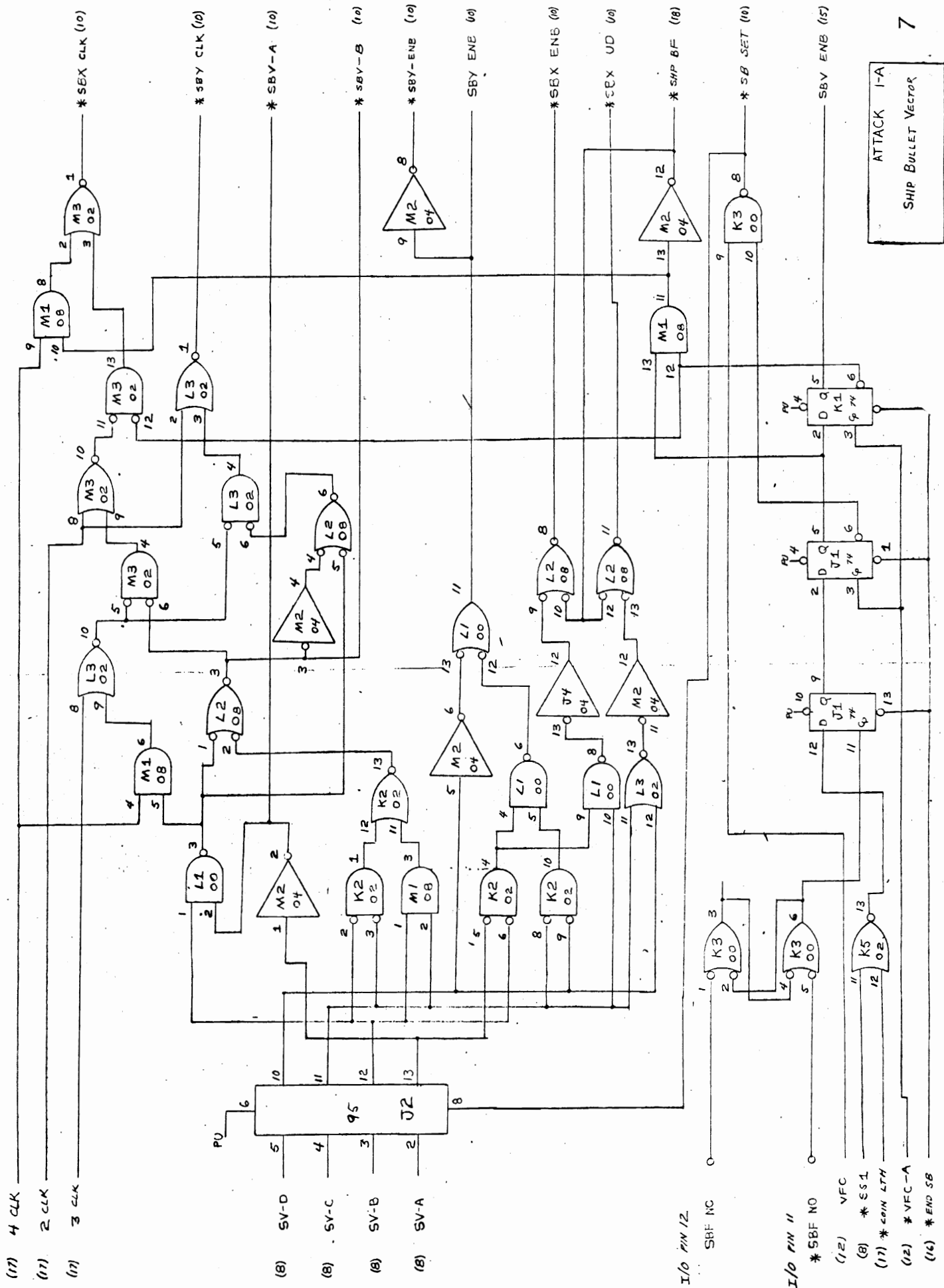


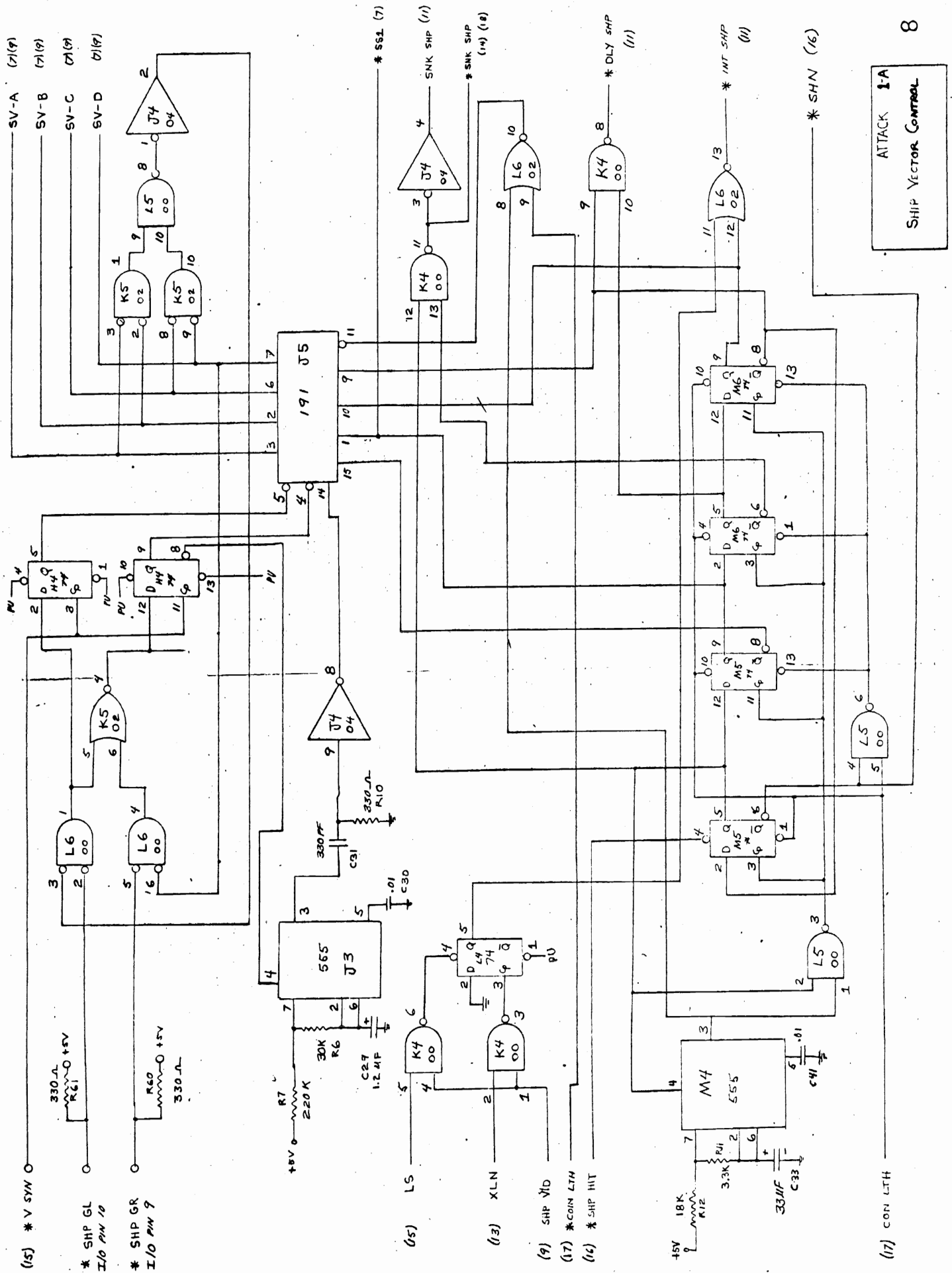
ATTACK I-A
PLANE BULLET VECTOR



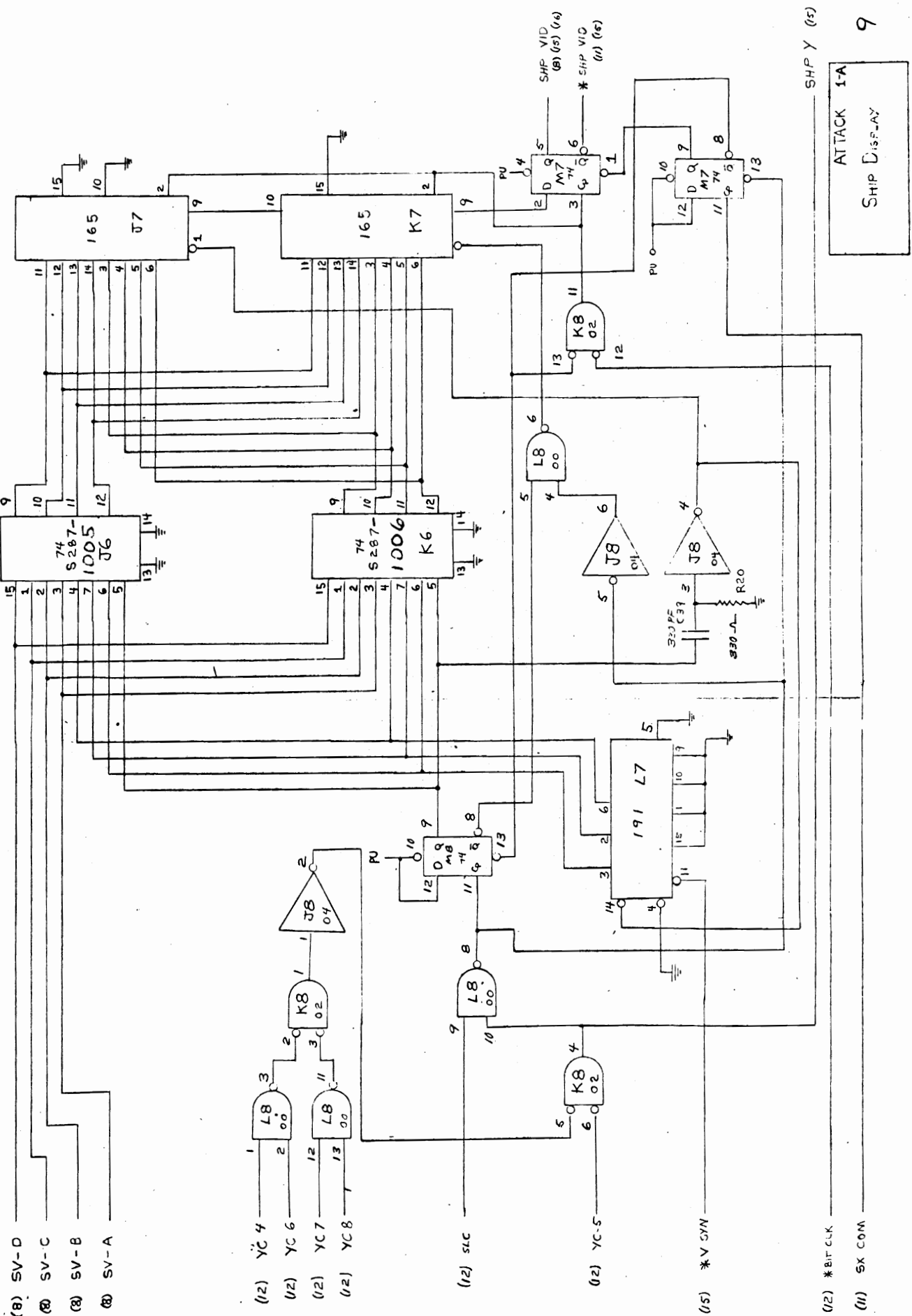


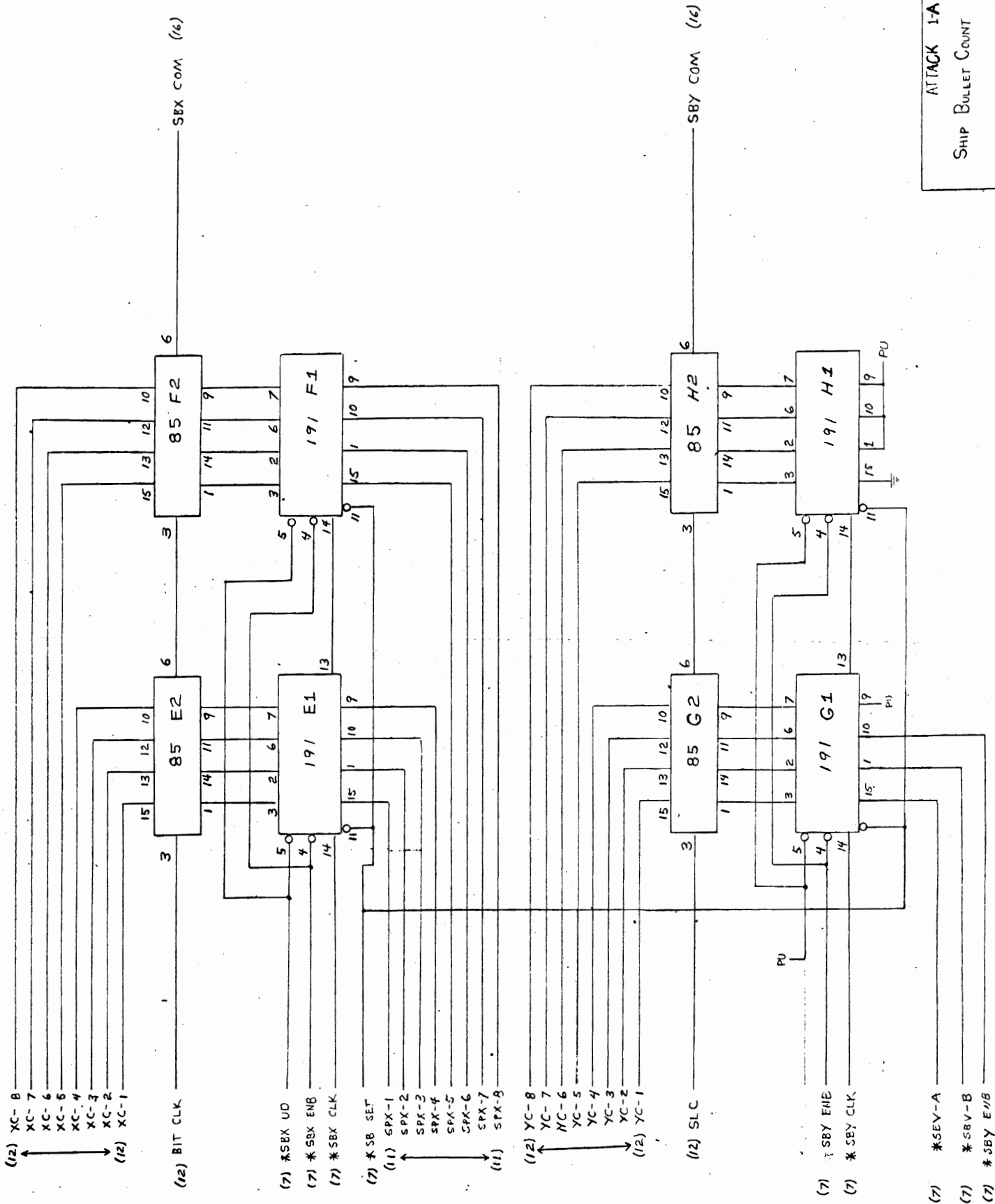
ATTACK 1-A
PLANE BULLET COUNT



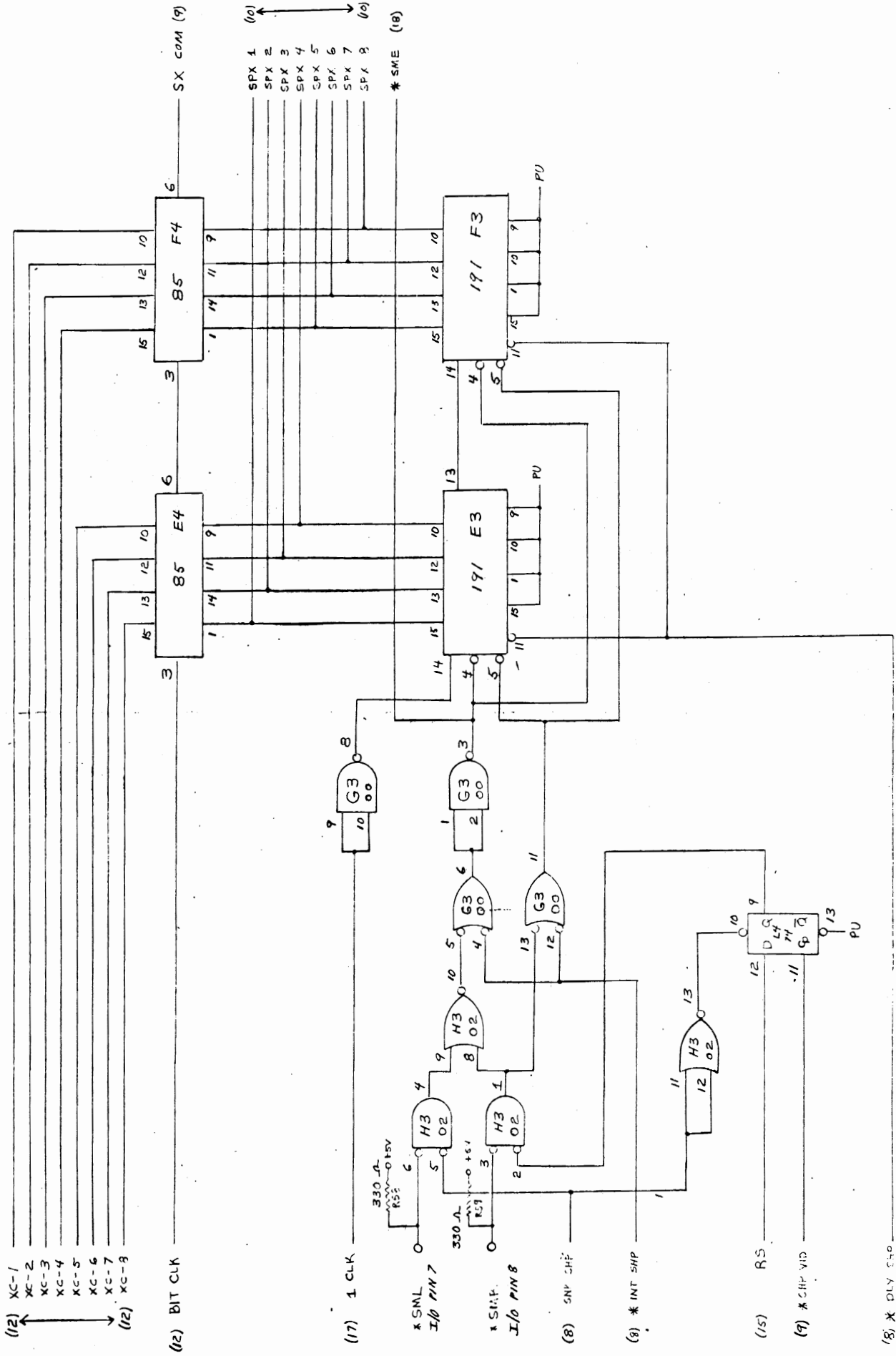


ATTACK 1-A
SHIP VECTOR CONTROL

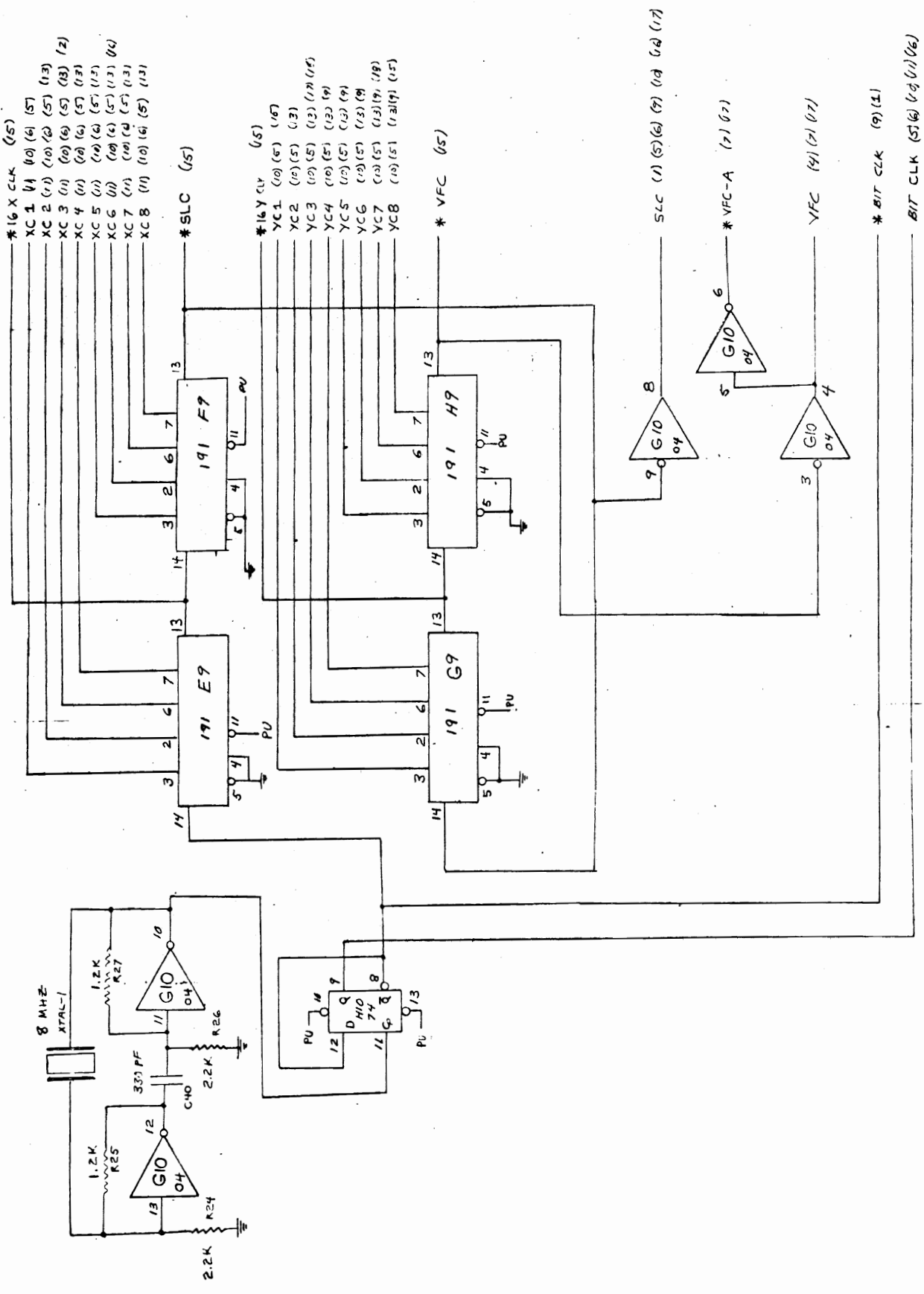




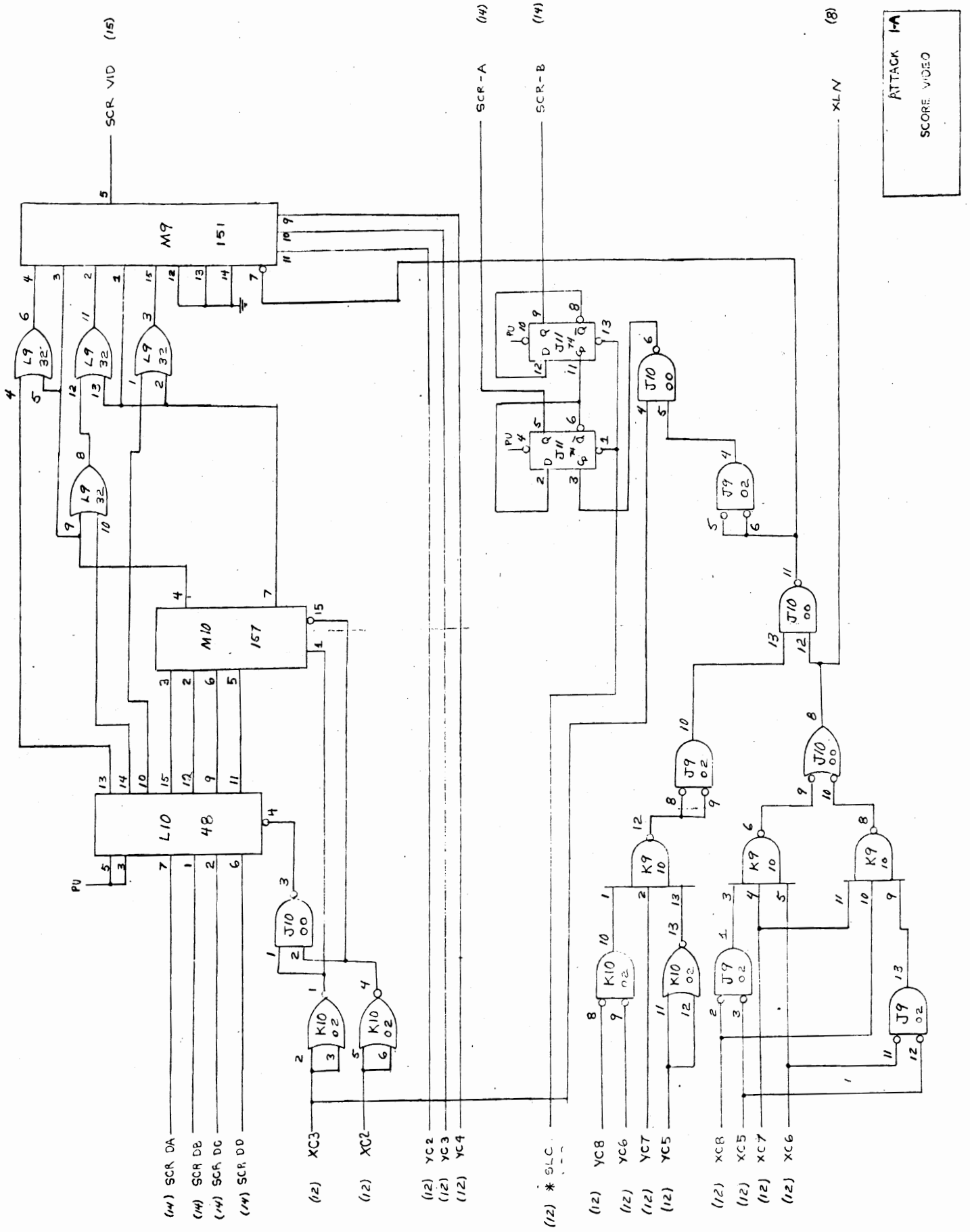
ATTACK 1-A
 SHIP BULLET COUNT

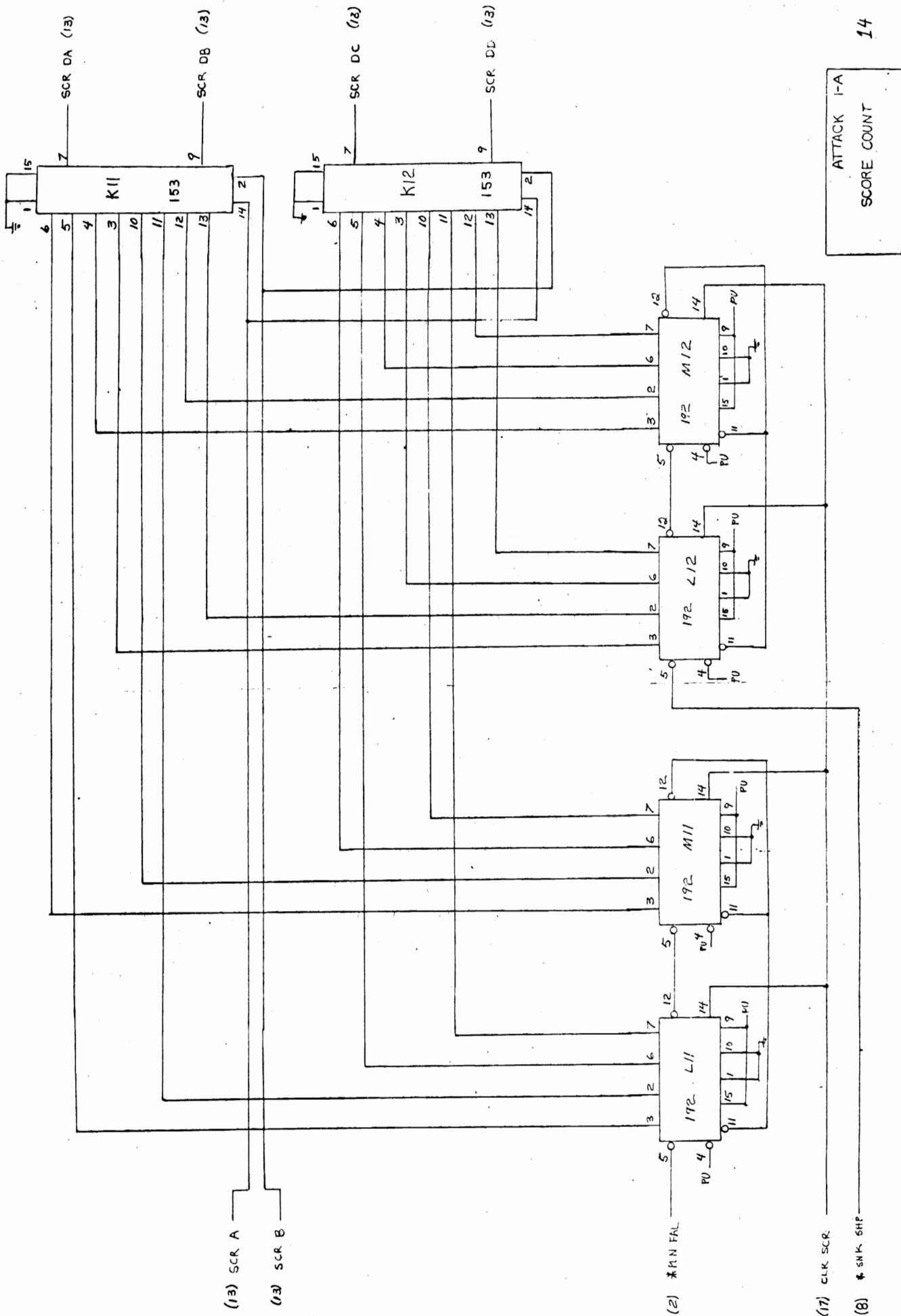


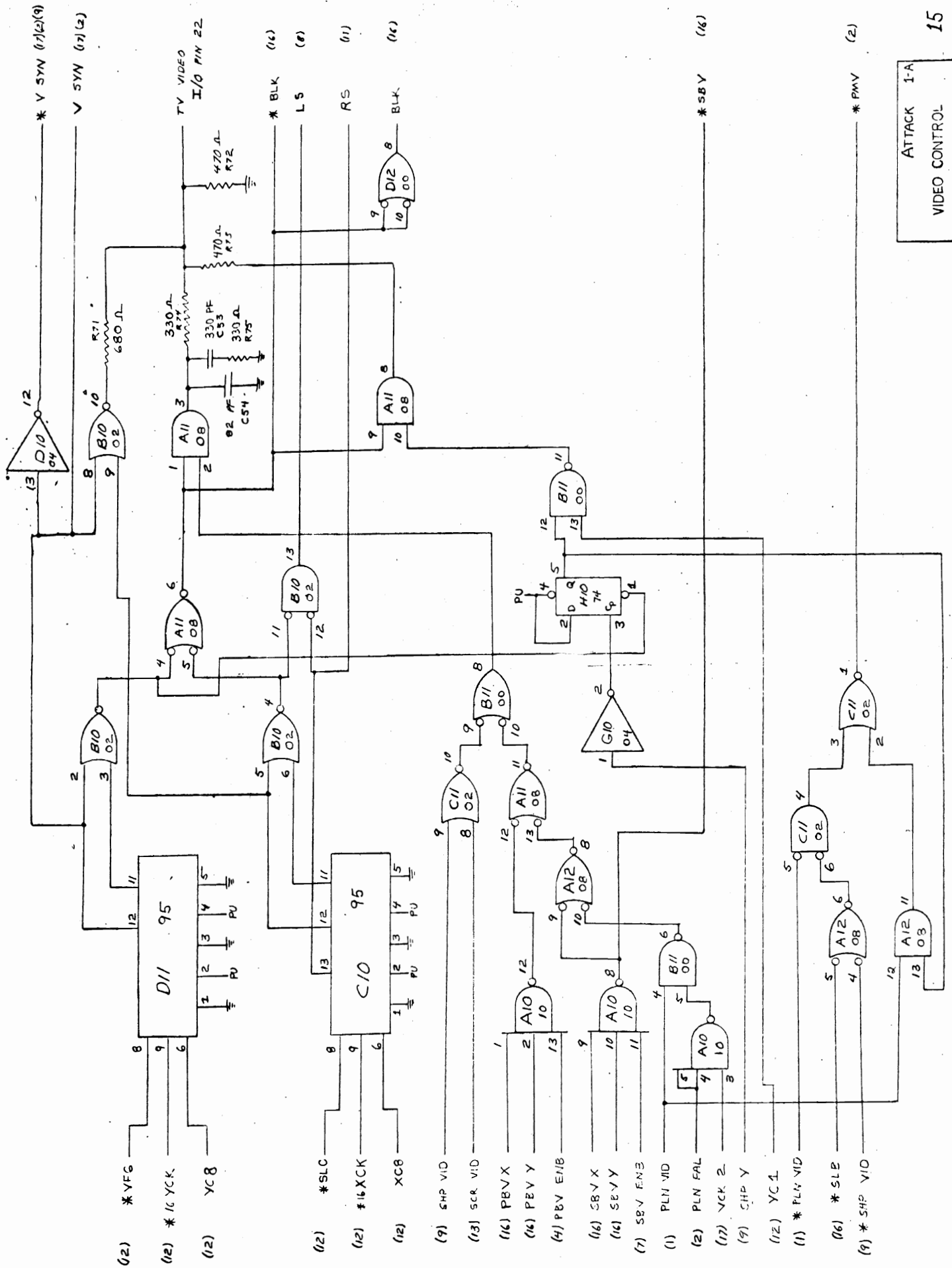
ATTACK 1-A
SHIP X COUNT



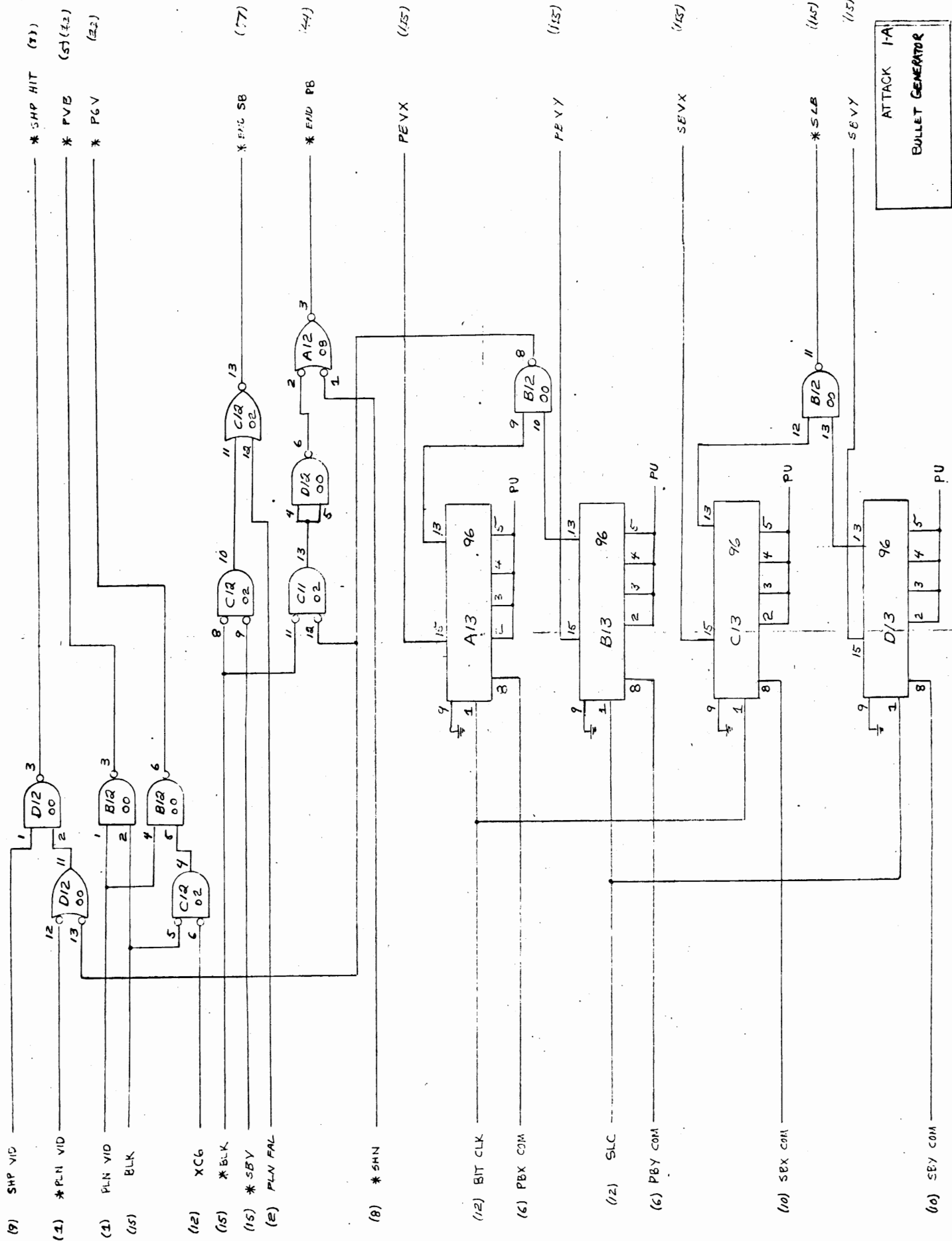
ATTACK
TIMING







ATTACK I-A
VIDEO CONTROL



ATTACK 1-A
BULLET GENERATOR

